



JSPM'S
Jayawantrao Sawant Polytechnic, Hadapsar
Department of Electronics &
Telecommunication Engineering



To,
The Principal,
J.S. Polytechnic, Hadapsar.
Pune-411028

Date: 25-03-2016

Subject: Module Committee (MC) and modules in Department of Electronics & Telecommunication Engineering.

Respected Sir,

With reference to the subject mentioned above, as per discussion & guideline given by ICIU in meeting held on 21 March 2016, the module coordinator committee is formed and modules are fixed for E&TC engineering department.

The role of this committee is as follows,

1. Formation of COs and TLOs
3. Semester planning for course delivery, design contest, workshop, expert lectures, site visits,
Mini projects, preparation of course file.
4. Evaluates and monitors the attainment of COs, POs
5. Proposes necessary changes for continuous improvements.
6. Preparation of periodic reports on course related activities.
7. Student motivation: Attend/participate tech competitions, paper presentation, mini

Projects/models, social /cultural events, skill development programs.

Following are the members of MC

➤ For ODD Semester

Sr. No/ Name of Module	Name of Subject	Name of Module Coordinator
01 Communication	Digital Communication	Ms. M.V.Kakade
	Audio Video Engineering	
02 Electrical & Electronics	Electronics Devices & Circuits	Mrs. S.B. Baraskar
	Electrical Engineering	
03	Principles of Digital Techniques	

VLSI & Embedded System	Microcontroller	Mr. V.C. Bhosale
	EDP & Project	
04 Measurement & Control	Electronic Instruments & Measurements	Mr. S.S. Mahulkar
	Control System & PLC	
05 Allied	Computer Fundamental	Mr. A.M.Naikwade
	Programming in C	
	Computer Hardware & Networking	
	Professional Practices –I	
	Professional Practices –III	
	Behavioural Science	
	Engineering Graphics	
Basic Workshop Practice		

For EVEN Semester

Sr. No/ Name of Module	Name of Subject	Name of Module Coordinator
01 Communication	Analog Communication	Ms. M.V.Kakade
	Advanced Communication Systems	
	Mobile Communication	
02 Electrical & Electronics	Elements of Electronics	Mrs. S.B. Baraskar
	Power Electronics	
	Linear Integrated Circuits	
03 VLSI & Embedded System	Embedded System	Mr. V.C. Bhosale
	Very Large scale Integration	
	Simulation Software	
	Industrial Project	

04 Measurement & Control	Industrial Measurements	Mr. S.S. Mahulkar
05 Allied	Management	Mr. A.M.Naikwade
	Environmental Studies	
	Professional Practices -II	
	Visual Basics	
	Electronic Workshop	

Sign: 
Name:
Designation: Program Coordinator


HOD
HEAD OF DEPARTMENT
Electronics & Telecommunications Dept
Jayawantrao Sawant Polytechnic
Hadapsar, Pune-411 028



JSPM'S
Jayawantrao Sawant Polytechnic, Hadapsar
Department of Electronics &
Telecommunication Engineering



Office order

Date: 25/03/2016

Dear Sir/Mam,

We pleased to inform that following members are appointed as **Module coordinator** of respective domain in Department of E&TC Engineering.

Sr. No	Name of Member	Name of Module
1.	Ms. M.V. Kakade	Communication
2.	Mrs. S.B. Baraskar	Electrical & Electronics
3.	Mr. V.C. Bhosale	VLSI & Embedded System
4.	Mr. S.S. Mahulkar	Measurement & Control
5.	Mr. A.M.Naikwade	Allied

Your responsibilities as Module Coordinator is as follows,


1. Formation of COs and TLOs
2. Formulation of curriculum gap and content beyond syllabi
3. Semester planning for course delivery, design contest, workshop, expert lectures, site visits, mini projects
4. Evaluates and monitors the attainment of COs, TLOs
5. Proposes necessary changes for continuous improvements.
6. Preparation of periodic reports on course related activities, status reports for management and key stakeholders.
7. Student motivation: Attend/participate tech competitions, paper presentation, mini projects/models, social/cultural events, skill development programs.

Sign:
Name

Designation, HOD

HEAD OF DEPARTMENT
Electronics & Telecommunications Dept
Jayawantrao Sawant Polytechnic
Hadapsar, Pune-411 028

Copy to: All members


Mr.N.B.Kardekar
Principal



JSPM'S
Jayawantrao Sawant Polytechnic, Hadapsar
Department of Electronics &
Telecommunication Engineering



To,
The Principal,
J.S Polytechnic, Hadapsar.
Pune-411028

Date: 25/03/2016

Subject: Formation of Departmental Advisory Board (DAB) in E&TC Engineering.

Respected Sir,

With reference to the subject mentioned above, as per discussion and guidelines given by ICIU in meeting held on 21 March 2016, we form DAB for the department of E&TC Engineering.

Following are the members of DAB

Sr. No.	Name of member	Designation
1	Mrs. M.M.Kulkarni	Chairperson
2	Ms. P.N. Padalkar	Program coordinator
3	Mrs. S.G.Deshmukh	Program coordinator
4	Mr. V.A.Bugade	Management representative
5	Ms. M.V. Kakade	Module coordinators-Communication Group
6	Mrs. S.B. Baraskar	Module coordinators-Electrical & Electronics Group
7	Mr. V.C. Bhosale	Module coordinators-VLSI & Embedded Group
8	Mr. S.S. Mahulkar	Module coordinators-Measurement & Control Group
9	Mr. A.M.Naikwade	Module coordinators-Allied Group
10	Mr. A.M. Patil	Faculty representative
11	Mr. N.R. Kumbhar	Industry representative
12	Mr. Ajinkyakumar Gadhave	Alumni representative
13	Ms. Pooja Patil	Student representative
14	Mrs. Asha Kamble	Parent representative
15	Mr. N.R. Kumbhar	R&D representative

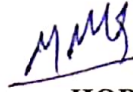
The role of this committee as follows,

1. Drafting of Vision, Mission of department
2. Drafting of PEOs, Formulation of POs
3. Defines current and future issues related to program.
4. Develop/recommends new or revised PEOs
5. Recommends the proposals/requirements for effective implementation of OBE
6. Define various assessment tools for measuring outcomes
7. Evaluates the attainment of PEOs, POs and proposes necessary improvements

Sign:

Name:

Designation: Program Coordinator



HOD

HEAD C
Electronics & Communication Dept
Yashwantrao Sawant Polytechnic
Hadapsar Pune-411 028



JSPM'S
Jayawantrao Sawant Polytechnic, Hadapsar
Department of Electronics &
Telecommunication Engineering



To,

Date: 25/03/2016

The Principal,
J.S Polytechnic, Hadapsar.
Pune-411028

Subject: Formation of Program Assessment Committee (PAC) for E&TC Engineering Department.

Respected Sir,


With reference to the subject mentioned above, as per discussion & guideline given by ICIU in meeting held on 21 March 2016, the Program Assessment Committee (PAC) is formed for E&TC engineering department.

The role of this committee as follows,

1. Evaluates and monitors the attainment of POs
2. Proposes necessary changes for continuous improvements.
3. Preparation of periodic reports on programme related activities, status reports for management and key stakeholders.
4. Faculty motivation: Attend/organize workshop/seminar/FDP, paper publication, development of models/lab.
5. Student motivation: Attend/participate tech competitions, paper presentation, mini projects/models, social/cultural events, skill development programs.
6. Conduct surveys, interaction with faculty, coordinators and other stakeholders
7. Planning of co-curricular activities for attainment of POs

Following are the members of PAC

Sr. No	Name of staff	Designation
1.	Ms. P.N. Padalkar	Program Coordinator
2.	Mrs. S.G.Deshmukh	Program Coordinator
3.	Ms. M.V. Kakade	Module Coordinator
4.	Mrs. S.B. Baraskar	Module Coordinator
5.	Mr. V.C. Bhosale	Module Coordinator
6.	Mr. S.S. Mahulkar	Module coordinator
7.	Mr. A.M.Naikwade	Module coordinator
8.	Mr. A.M. Patil	Faculty Representative

Sign: 
Name:
Designation: Program Coordinator


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